

DE 244

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10017888	FILING DATE 12/12/2001	CLASS 375	SUBCLASS 376	GAU 2631	EXAMINER BURD KHAI TRAN
**APPLICANTS: Ruuskanen Markku;					
**CONTINUING DATA VERIFIED: YES THIS APPLICATION IS A CON OF PCT/FI00/00557 06/21/2000 ML					
** FOREIGN APPLICATIONS VERIFIED: NO YES FINLAND 991507 07/01/1999 ML					
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EG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials ML		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 796.422USWI	
TITLE : Control of phase locked loop during change of synchronisation source U.S. DEPT. OF COMM. / PAT. & TM. - PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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